

Notice of Allowability

Application No.

10/062,047

Examiner

Binh C. Tat

Applicant(s)

TEIG ET AL.

Art Unit

2825

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment on 01/23/04.
2. ☒ The allowed claim(s) is/are 1-8,10,13-15 and 17-24.
3. ☒ The drawings filed on 23 January 2004 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date 01/23/04, 03/16/04
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date _____.
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____.

DETAILED ACTION

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Gregory Suh on 04/16/04.

The application has been amended as follow:

Claim 1 line 4 change "repeatedly" to "iteratively".

Claim 1 line 7 after "sub-networks", insert: – that are bound to a particular technology--.

Claim 1 line 10 deleted " wherein at least a particular one of the replacement sub-networks has multiple circuit elements, wherein at least one of the multiple circuit elements of the particular replacement sub-network is independently selectable by the method in a later iteration of the method for inclusion in a candidate sub-network", insert: – wherein, during an iteration, a first candidate sub-network is replaced in the design with a first replacement sub-network, the first replacement sub-network being comprised of multiple circuit elements, and during another iteration, a second candidate sub-network is replaced by a second replacement sub-network, the second candidate sub-network being comprised of at least one but not all circuit elements of the first replacement sub-network--.

Claim 6 line 2 change "repetitions" to "iterations".

Art Unit: 2825

Claim 7 line 3 deleted “and the storage structure stores replacement sub-networks that are bound to the technology,”.

Claim 7 line 5 change “repetitions” to “iterations”.

Claim 15 line 9 after “sub-network”, insert: --;

c) selecting from the design a second candidate sub-network that includes at least one but not all circuit elements of the first replacement sub-network; and

d) replacing the second candidate sub-network in the design with a second replacement sub-network, the second replacement sub-network being stored in the storage structure--.

Claim 16 has been canceled.

Claim 22 lines 1 change “claim 19” to “claim 21”.

Claim 23 lines 1 change “claim 19” to “claim 21”.

Reasons for Allowance

The following is an examiner's statement of reasons for allowance:

Claims 1-8, 10, 13-15, and 17-24 are allowed because the prior art does not teach or suggest a method for performing technology mapping, the method comprising: a) receiving a design; b) iteratively: selecting from the design a candidate sub-network; identifying at least one replacement sub-network from a storage structure that stores replacement sub-networks that are bound to a particular technology; and replacing the selected candidate sub-network in the design with the replacement sub-network; wherein, during an iteration, a first candidate sub-network is replaced in the design with a first replacement sub-network, the first replacement sub-network being comprised of multiple circuit elements, and during another iteration, a second candidate sub-network is replaced by a second replacement sub-network, the second candidate sub-network being comprised of at least one but not all circuit elements of the first replacement sub-network.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance".

Conclusion

Art Unit: 2825

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Binh Tat whose telephone number is (571) 272-1908. The examiner can normally be reached on Monday to Friday from 8:30am to 5:00pm.

4. Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is (703) 308-1782.

Tat Binh

Patent Examiner

April, 17 2004



VUTHE SIEK
PRIMARY EXAMINER